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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/579,051	05/09/2006	Terry L. Alford	07-1233-US	6367
David S. Harpe	7590 04/03/200 r	EXAMINER		
-	ehnen Hulbert & Bergh	ISAAC, STANETTA D		
300 South Wac	ker Drive	ART UNIT	PAPER NUMBER	
Chicago, IL 600	506	2812		
			MAIL DATE	DELIVERY MODE
			04/03/2008	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

		Ι Α,	plication No.	1	Applicant(s)			
Office Action Summers								
			0/579,051		ALFORD ET AL.			
Office Action Summary			aminer		Art Unit			
			ANETTA D. ISAAC		2812			
Period fo	The MAILING DATE of this commun r Reply	nication appears	s on the cover sheet	with the co	rrespondence ad	ldress		
WHIC - Exten after: - If NO - Failur Any re	DRTENED STATUTORY PERIOD F HEVER IS LONGER, FROM THE M sions of time may be available under the provisions SIX (6) MONTHS from the mailing date of this comr period for reply is specified above, the maximum st e to reply within the set or extended period for reply eply received by the Office later than three months of d patent term adjustment. See 37 CFR 1.704(b).	MAILING DATE s of 37 CFR 1.136(a) munication. tatutory period will ap y will, by statute, caus	OF THIS COMMUN. In no event, however, may ply and will expire SIX (6) More the application to become	NICATION. TO A reply be time TONTHS from the ABANDONED	ly filed e mailing date of this o (35 U.S.C. § 133).			
Status								
1) 又	Responsive to communication(s) file	ed on 00 May 1	2006					
·	• •							
	This action is FINAL . 2b) This action is non-final.							
,	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
	closed in accordance with the practi	ice under £x p	arte Quayre, 1900 O	J.D. 11, 400	0.0.213.			
Dispositi	on of Claims							
4)🛛	☑ Claim(s) <u>1-29</u> is/are pending in the application.							
4	4a) Of the above claim(s) is/are withdrawn from consideration.							
5)	5) Claim(s) is/are allowed.							
6)⊠)⊠ Claim(s) <u>1-29</u> is/are rejected.							
7)								
8)□	Claim(s) are subject to restric	ction and/or ele	ection requirement.					
Applicati	on Papers							
9)□ -	The specification is objected to by th	e Examiner.						
· —	10)⊠ The drawing(s) filed on <u>09 May 2006</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.							
•	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).								
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.								
Priority u	nder 35 U.S.C. § 119	·						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:								
	1. Certified copies of the priority documents have been received.							
	2. Certified copies of the priority documents have been received in Application No							
	3. Copies of the certified copies of the priority documents have been received in this National Stage							
	application from the International Bureau (PCT Rule 17.2(a)).							
* See the attached detailed Office action for a list of the certified copies not received.								
Attachment	(s)							
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)								
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Notice of Draftsperson's Patent Drawing Review (PTO-948) Paper No(s)/Mail Date Notice of Informal Patent Application								
	Paper No(s)/Mail Date <u>5/09/06 & 10/05/06</u> .							
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DETAILED ACTION

This Office Action is in response to the application filed on 5/09/06. Currently, claims 1-29 are pending.

Information Disclosure Statement

The information disclosure statements (IDS) were submitted on 5/09/06 and 10/05/. The submission is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

Specification

The specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 1-29 are rejected under 35 U.S.C. 102(e) as being anticipated by Cayrefourcq et al.US Patent 7,176,108.

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3. Cayrefourcq discloses the semiconductor device and method as claimed. See figures 1-3, and corresponding text, where Cayrefourcq teaches, pertaining to claims 1, 8, 14 and 22, a flat panel display, electronic device, electronic apparatus and method of forming an electronic apparatus, comprising: a flexible substrate 7 (col. 7, lines 55-60) a single crystalline silicon substrate 5 disposed adjacent to the flexible substrate (figure 2; col. 7, lines 14-17 and 53-55), wherein the flexible substrate is bonded to the single crystalline substrate using an ion implantation process (figures 2 and 3; col. 7, lines 4-52, *Note*: the Examiner takes the position the ion implantation process is used to form the thin layer that is attached to the flexible substrate); and a plurality of semiconductor devices formed on the single crystalline silicon substrate (col. 1, lines 12-16).

- 4. Cayrefourcq teaches, pertaining to claims 2, 9, 17 and 25, wherein the ion implantation process uses a noble gas (col. 6, lines 55-59).
- 5. Cayrefourcq teaches, pertaining to claims 3, 10, 18, and 26, wherein the ion implantation process uses a gas selected from the group consisting of hydrogen, helium, xenon, and krypton (col. 6, lines 55-59).
- 6. Cayrefourcq teaches, pertaining to claims 4, 11, 19 and 27, wherein the flexible substrate includes a polymer material (col. 7, lines 55-60).
- 7. Cayrefourcq teaches, pertaining to claims 5, 12, 20 and 28, wherein the flexible substrate includes a material selected from the group consisting of polymer, plastic, paper, flexible glass, and stainless steel (col. 7, lines 55-60).

Cayrefourcq teaches, pertaining to claims 6, 13, 21 and 29, wherein the plurality of semiconductor devices includes thin film transistors (col. 1, lines 12-16).

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to STANETTA D. ISAAC whose telephone number is (571)272-1671. The examiner can normally be reached on Monday-Friday 9:30am -6:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Lebentritt can be reached on 571-272-1873. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Stanetta Isaac Patent Examiner March 30, 2008 /Walter L. Lindsay, Jr./ Primary Examiner, Art Unit 2812